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In re Application of Applicants:

Date: November 10, 2004

Beaman et al.

Group Art Unit: 3729

Serial No.: 09/921,867

Examiner: A. D. Tugbang

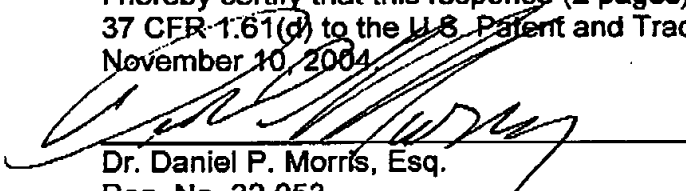
Filed: August 3, 2001

Docket No.: YOR919930028US6

For: HIGH DENSITY INTEGRATED CIRCUIT APPARATUS,
TEST PROBE AND METHODS OF USE THEREOFCommissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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I hereby certify that this response (2 pages) is being facsimile transmitted under Rule 37 CFR 1.61(d) to the U.S. Patent and Trademark Office to (703) 872-9306 on November 10, 2004.


Dr. Daniel P. Morris, Esq.
Reg. No. 32,053**RESPONSE TO RESTRICTION REQUIREMENT**
DATED OCTOBER 25, 2004

REMARKS

Applicants elect, without traverse, Claim Group IA (claims 29-62). Claims 29-62 are substantially identical to the claims of US 6,336,269.

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Respectfully submitted,

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